

SPECIFICATIONS

VME CONTROL

VME Device Type: A24/D16, D08 (EO) Slave.
Base Address: Switch selectable, A23-A16.
Address Modifiers: Standard supervisory block transfer (3F). Standard supervisory data access (3D). Standard non-privileged block transfer (3B). Standard non-privileged data access (39).

CONTROL REGISTERS

CSR0: Control and status.
CSR1: Next transfer address generator.
CSR2: Word counter.
CSR3: Destination address register.

FRONT PANEL

Displays: PWR - Red LED indicates +5 V and -12 V present. ADR - Yellow LED blinks when module is addressed. RUN - Green LED indicates data transfer under 1131 control is in progress. ERR - Red LED indicates an error has occurred.

Trigger Input: 5 x 2 differential ECL, 112 Ω termination;

Pins 1,2 Trigger strobe, rising edge causes RUN condition when enabled.

Pins 3,4 }
Pins 5,6 } Trigger offset number to internal
Pins 7,8 } memory.
Pins 9,10 Not connected.

SYSTEMS INTERFACE BUS (SIB)

SIB Connector: SIB port connection, TTL levels (adapter card and cable are not included).

Device Type: Master, complete SIB control capability.

Speed: 5 Mbytes/sec, maximum (speed is cable length and slave dependent).

GENERAL

Operating Modes: SIB direct, single transfer Read or Write of the SIB under VME (program) control; Local DMA, multiple transfer sequential Read or Write of the SIB under 1131 control; List, multiple transfer random Read and/or Write of multiple SIB modules.

Packaging: 6 U x 160 mm single-width VME, in conformance with VME Specification (ANSI/IEEE-1014C).

Operating Temperature: 10° to 50° C.

Storage Temperature: -30° to 85° C.

Maximum Humidity: 80% non-condensing at 30° C.

Power: 3 A at +5 V; 0.2 A at -12 V.

ACCESSORIES

DCSIB/60-L: SIB cable, high density 60 conductor .025 flat ribbon. From the 1131 to the 1445A, 1821 or 3031. Specify length in feet.

ADP-SIB34/60: SIB to SM/I adaptor card required for the 1821.

Allocation of Address Space

Byte Address	Function Implementation
0xFFFF : 0xC000 0xBFFF : 0x8000 0x7FFF : 0x4000 0x3FFF : 0x1000 0x0FFF : 0x0800 0x07FF : 0x0008 0x0006 0x0004 0x0002 0x0000	} Reserved } Data Memory } List Memory } Reserved } SIB Direct Access Mapping } Reserved Destination Address Register Word Counter Next Transfer Address Generator Control and Status Register